•	Application No.	Applicant(s)	•
Notice of Allowability	10/656,451	BAUM ET AL.	
	Examiner	Art Unit	
	N. Drew Richards	2815	
	14. Diew Michards	2013	
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due c	d ourse. <b>THIS</b>
1. X This communication is responsive to application filed 9/5/0.	<u>3</u> .		
2. The allowed claim(s) is/are <u>1-4,6-17 and 19-29</u> .			
3. A The drawings filed on <u>05 September 2003</u> are accepted by	the Examiner.		
<ul> <li>4. ☐ Acknowledgment is made of a claim for foreign priority un</li> <li>a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents have</li> </ul>			
2. Certified copies of the priority documents have	been received in Application No		
3.  Copies of the certified copies of the priority doc	cuments have been received in this r	national stage applicati	on from the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requ	uirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give			OTICE OF
6.   CORRECTED DRAWINGS ( as "replacement sheets") mus	t be submitted.		
(a) $\square$ including changes required by the Notice of Draftspers	on's Patent Drawing Review ( PTO-	948) attached	
1) ☐ hereto or 2) ☐ to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the O	ffice action of	
Identifying indicia such as the application number (see 37 CFR 1, each sheet. Replacement sheet(s) should be labeled as such in the			oack) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT			ote the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. Notice of Informal Page	atent Application (PTO	-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary	, ,	,
3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Dat	e	
Paper No./Mail Date <u>9/5/03</u> 4. Examiner's Comment Regarding Requirement for Deposit	8.   Examiner's Stateme	nt of Reasons for Allov	vance
of Biological Material	9.		
	112200	/homes	
TOM THOMAS SUPERVISORY PATENT EXAMINER			

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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Dan Swayze on 2/24/05.

The application has been amended as follows:

In the claims:

- Claim 1 line 7, delete "a higher amount of"
- Claim 3 line 17, delete "a higher amount of"
- Cancel claim 5
- Claim 6 line 1, delete "claim 5" and replace therewith --claim 3--
- Claim 7 line 1, delete "claim 5" and replace therewith --claim 3--
- Claim 9 line 1, delete "claim 5" and replace therewith --claim 3--
- Claim 17 line 8, delete "a higher amount of"
- Cancel claim 18
- Claim 19 line 1, delete "claim 18" and replace therewith --claim 17--
- Claim 26 line 1, delete "an integrated circuit" and replace therewith —A
   method of manufacturing an integrated circuit—
- Claim 26 line 2, delete "means for"
- Claim 26 line 3, delete "means for"

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Claim 26 line 5, delete "means for"

• Claim 27 line 1, delete "integrated circuit" and replace therewith --method--

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- Claim 27 line 1, after "including" insert --forming--
- Claim 28 line 1, delete "integrated circuit" and replace therewith --method--
- 2. The following is an examiner's statement of reasons for allowance: Prior art of record fails to teach, disclose, or suggest, either alone or in combination, the integrated circuit as claimed in claims 1 and 3, the method of reducing peak hot spot temperatures as claimed in claim 17, the method of manufacturing an integrated circuit as claimed in claim 26, or the method of reducing harmonic distortion as claimed in claim 29.

  Specifically, the prior art of record fails to teach a structure or method of forming or using the structure, including a p-channel transistor composed of a first group of sections, an n-channel transistor composed of a second group of sections, and the sections of the first group being alternately located with respect to the sections of the second group so as to form a generally checkerboard pattern.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Miyaoka et al. (U.S. Patent No. 5220187), Aldrich (U.S. Patent

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No. 6566720 B2), Possley (US 2002/0070391 A1), Anderson et al. (U.S. Patent No.

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5459340), Kitagawa et al. (U.S. Patent No. 5489860).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to N. Drew Richards whose telephone number is (571) 272-1736. The examiner can normally be reached on Monday-Friday 9:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

us QV

TOM THOMAS

SUPERVISORY PATENT EXAMINER